

## SPECIFICATION

- o On page 1, after line 1, insert the following heading:

### Field of the Invention

- o On page 3, after line 12, insert the following heading:

### SUMMARY OF THE INVENTION

- o On page 6, after line 9, insert the following heading:

### BRIEF DESCRIPTION OF THE DRAWINGS

- o On page 8, after line 10, insert the following paragraph:

Figure 22 is a block diagram of a basic dual-loop predistorter.

- o On page 8, after line 12, insert the following heading:

### DETAILED DESCRIPTION

- o Amend paragraph beginning at page 9, line 3, as follows:

The RF signal entering the distortion path 215 is fed into a distortion generation circuit 230 which operates on the RF input signal to generate a set of non-linear distortion components each corresponding to a particular order of distortion. In Figure 1, the orders of distortion generated at the three output paths of the distortion generation circuit 230 are third order, fifth order, and seventh order, illustrated as frequency spectra in Figures ~~9b, 9c and 9d~~ 10b, 10c, and 10d, respectively. It is also possible for the distortion generation circuit to generate higher order distortion components such as ninth order, or to generate only third order, or third and fifth order distortion components.